Translation of the attached sheet (Japanese text portions only)

Background Art Information

Date etc

Inventor(s)/Author(s)

Patent No./Publication

*Concise Explanation				
Jpn. Pat. Appln.	KOKAI Publication	on No. 9-237	945; Publisl	hed
September 9, 1997	; "PRINTED WIRI	ING BOARD	AND COM	PONENT
MOUNTING DEV	TCE"; Daikin Indu	stries, Ltd.		
*Concise Explanation				
This publication describes a method for identifying a wiring board				
using jumpers. The method is similar to that described in the				
"Background Art" section of the present invention.				
Jpn. Pat. Appln. KOKAI Publication No. 7-282563; Published October				
27, 1995; "Cassette"; TDK Corporation				
*Concise Explanation				
This publication differs from the present invention, in that an				
electrical resistance circuit board which is to be identified and an object				
which is used for the identification purpose must be manufactured				
independently and then combined with each other.				
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Inventor(s)				
Signature & Date				
Patent engineer's commen	t on inventor's information	or patent engineer	's information	
None				
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Checked by	Dated			
	Toshiba Reference	Japanese Agent's	s Ref she	et